


PALM INTRANET

 Day : Saturday
 Date: 8/30/2008
 Time: 13:27:05

Inventor Name Search Result

Your Search was:

Last Name = **BEDICHEK**First Name = **ROBERT**

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08807542	5905855	150	02/28/1997	METHOD AND APPARATUS FOR CORRECTING ERRORS IN COMPUTER SYSTEMS	BEDICHEK, ROBERT
09417332	Not Issued	77	10/13/1999	METHOD FOR INTEGRATION OF INTERPRETATION AND TANSLATION IN A MICROPROCESSOR	BEDICHEK, ROBERT
09417980	6990658	150	10/13/1999	METHOD FOR TRANSLATING INSTRUCTIONS IN A SPECULATIVE MICROPROCESSOR FEATURING COMMITTING STATE	BEDICHEK, ROBERT
09417981	6415379	150	10/13/1999	METHOD AND APPARATUS FOR MAINTAINING CONTEXT WHILE EXECUTING TRANSLATED INSTRUCTIONS	BEDICHEK, ROBERT
09471447	6845353	150	12/23/1999	INTERPAGE PROLOGUE TO PROTECT VIRTUAL ADDRESS MAPPINGS	BEDICHEK, ROBERT
09539987	6594821	150	03/30/2000	TRANSLATION CONSISTENCY CHECKING FOR MODIFIED TARGET INSTRUCTIONS BY COMPARING TO ORIGINAL COPY	BEDICHEK, ROBERT
10463846	7096460	150	06/16/2003	SWITCHING TO ORIGINAL MODIFIABLE INSTRUCTION COPY COMPARISON CHECK TO VALIDATE PRIOR TRANSLATION WHEN TRANSLATED SUB-AREA PROTECTION EXCEPTION SLOWS DOWN OPERATION	BEDICHEK, ROBERT
11069497	Not Issued	71	02/28/2005	Method for translating instructions in a speculative microprocessor	BEDICHEK, ROBERT
11110085	Not Issued	61	01/18/2005	Interpage prologue to protect virtual address mappings	BEDICHEK, ROBERT
11507779	7404181	150	08/21/2006	SWITCHING TO ORIGINAL CODE	BEDICHEK, ROBERT

				COMPARISON OF MODIFIABLE CODE FOR TRANSLATED CODE VALIDITY WHEN FREQUENCY OF DETECTING MEMORY OVERWRITES EXCEEDS THRESHOLD	
07046633	4803622	150	05/07/1987	PROGRAMMABLE I/O SEQUENCER FOR USE IN AN I/O PROCESSOR	BEDICHEK, ROBERT C.

Inventor Search Completed: No Records to Display.

Search Another: Inventor

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PALM INTRANET

 Day : Saturday
 Date: 8/30/2008
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Inventor Name Search Result

Your Search was:

Last Name = KEPPEL

First Name = DAVID

Application#	Patent#	Status	Date Filed	Title	Inventor Name
08807542	5905855	150	02/28/1997	METHOD AND APPARATUS FOR CORRECTING ERRORS IN COMPUTER SYSTEMS	KEPPEL, DAVID
09298762	6203541	150	04/23/1999	AUTOMATIC ACTIVATION OF ELECTROSURGICAL GENERATOR BIPOLAR OUTPUT	KEPPEL, DAVID
09415550	6402741	150	10/08/1999	CURRENT AND STATUS MONITOR	KEPPEL, DAVID
09417332	Not Issued	77	10/13/1999	METHOD FOR INTEGRATION OF INTERPRETATION AND TANSLATION IN A MICROPROCESSOR	KEPPEL, DAVID
09417356	6363336	150	10/13/1999	FINE GRAIN TRANSLATION DISCRIMINATION	KEPPEL, DAVID
09417358	6714904	150	10/13/1999	SYSTEM FOR USING RATE OF EXCEPTION EVENT GENERATION DURING EXECUTION OF TRANSLATED INSTRUCTIONS TO CONTROL OPTIMIZATION OF THE TRANSLATED INSTRUCTIONS	KEPPEL, DAVID
09417930	6356615	150	10/13/1999	PROGRAMMABLE EVENT COUNTER SYSTEM	KEPPEL, DAVID
09417981	6415379	150	10/13/1999	METHOD AND APPARATUS FOR MAINTAINING CONTEXT WHILE EXECUTING TRANSLATED INSTRUCTIONS	KEPPEL, DAVID
09421615	6738892	150	10/20/1999	USE OF ENABLE BITS TO CONTROL EXECUTION OF SELECTED INSTRUCTIONS	KEPPEL, DAVID
09464644	6513110	150	12/15/1999	CHECK INSTRUCTION AND METHOD	KEPPEL, DAVID
09464661	6668287	150	12/15/1999	SOFTWARE DIRECT MEMORY ACCESS	KEPPEL, DAVID

09471447	6845353	150	12/23/1999	INTERPAGE PROLOGUE TO PROTECT VIRTUAL ADDRESS MAPPINGS	KEPPEL, DAVID
09822933	6829719	150	03/30/2001	METHOD AND APPARATUS FOR HANDLING NESTED FAULTS	KEPPEL, DAVID
10427832	7137980	150	05/01/2003	METHOD AND SYSTEM FOR CONTROLLING OUTPUT OF RF MEDICAL GENERATOR	KEPPEL, DAVID
10464870	Not Issued	160	06/18/2003	Software direct memory access	KEPPEL, DAVID
10670900	Not Issued	161	09/24/2003	Controlling instruction translation using dynamic feedback	KEPPEL, DAVID
10980127	Not Issued	93	11/01/2004	METHOD AND APPARATUS FOR HANDLING NESTED FAULTS	KEPPEL, DAVID
11110085	Not Issued	61	01/18/2005	Interpage prologue to protect virtual address mappings	KEPPEL, DAVID
11431449	Not Issued	30	05/10/2006	System and method for reducing leakage current in an electrosurgical generator	KEPPEL, DAVID
11585506	Not Issued	30	10/24/2006	Method and system for controlling output of RF medical generator	KEPPEL, DAVID
61037243	Not Issued	20	03/17/2008	Variable Capacitive Electrode Pad	KEPPEL, DAVID
10728211	7033354	150	12/04/2003	ELECTROSURGICAL ELECTRODE HAVING A NON-CONDUCTIVE POROUS CERAMIC COATING	KEPPEL, DAVID S.
10728212	7044948	150	12/04/2003	CIRCUIT FOR CONTROLLING ARC ENERGY FROM AN ELECTROSURGICAL GENERATOR	KEPPEL, DAVID S.
10728259	7255694	150	12/04/2003	VARIABLE OUTPUT CREST FACTOR ELECTROSURGICAL GENERATOR	KEPPEL, DAVID S.
11338479	Not Issued	41	01/24/2006	Method and system for transmitting data across patient isolation barrier	KEPPEL, DAVID S.
11355725	7223265	150	02/16/2006	ELECTROSURGICAL ELECTRODE HAVING A NON-CONDUCTIVE POROUS CERAMIC COATING	KEPPEL, DAVID S.
11367514	Not Issued	30	03/03/2006	Circuit for controlling arc energy from an electrosurgical generator	KEPPEL, DAVID S.
11728772	Not Issued	93	03/27/2007	ELECTROSURGICAL ELECTRODE HAVING A NON-CONDUCTIVE POROUS CERAMIC COATING	KEPPEL, DAVID S.
60432384	Not Issued	159	12/10/2002	Circuit for controlling arc energy from an electrosurgical generator	KEPPEL, DAVID S.
60432385	Not Issued	159	12/10/2002	Electrosurgical electrode having a non-conductive porous ceramic coating	KEPPEL, DAVID S.
60432391	Not	159	12/10/2002	Variable output crest factor electrosurgical	KEPPEL, DAVID S.

Issued

generator

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Inventor Name Search Result

Your Search was:

Last Name = TORVALDS

First Name = LINUS

Application#	Patent#	Status	Date Filed	Title	Inventor Name
09417332	Not Issued	77	10/13/1999	METHOD FOR INTEGRATION OF INTERPRETATION AND TRANSLATION IN A MICROPROCESSOR	TORVALDS, LINUS
09417358	6714904	150	10/13/1999	SYSTEM FOR USING RATE OF EXCEPTION EVENT GENERATION DURING EXECUTION OF TRANSLATED INSTRUCTIONS TO CONTROL OPTIMIZATION OF THE TRANSLATED INSTRUCTIONS	TORVALDS, LINUS
09417979	6880152	150	10/13/1999	METHOD OF OPTIMIZING COMPILER VS INTERPRETER MODE OPERATION IN A CODE TRANSLATOR BASED UPON AMOUNT OF TIME SPENT IN EACH MODE	TORVALDS, LINUS
09417980	6990658	150	10/13/1999	METHOD FOR TRANSLATING INSTRUCTIONS IN A SPECULATIVE MICROPROCESSOR FEATURING COMMITTING STATE	TORVALDS, LINUS
09539987	6594821	150	03/30/2000	TRANSLATION CONSISTENCY CHECKING FOR MODIFIED TARGET INSTRUCTIONS BY COMPARING TO ORIGINAL COPY	TORVALDS, LINUS
09596279	6615300	150	06/19/2000	FAST LOOK-UP OF INDIRECT BRANCH DESTINATION IN A DYNAMIC TRANSLATION SYSTEM	TORVALDS, LINUS
10463233	7111096	150	06/17/2003	FAST LOOK-UP OF INDIRECT BRANCH DESTINATION IN A DYNAMIC TRANSLATION SYSTEM	TORVALDS, LINUS
10463846	7096460	150	06/16/2003	SWITCHING TO ORIGINAL MODIFIABLE INSTRUCTION COPY COMPARISON CHECK TO VALIDATE PRIOR TRANSLATION WHEN TRANSLATED SUB-AREA	TORVALDS, LINUS

PROTECTION EXCEPTION SLOWS DOWN OPERATION					
10609158	Not Issued	41	06/27/2003	Method and system for supporting input/output for a virtual machine	TORVALDS, LINUS
10670900	Not Issued	161	09/24/2003	Controlling instruction translation using dynamic feedback	TORVALDS, LINUS
10719879	Not Issued	41	11/20/2003	Architecture, system, and method for operating on encrypted and /or hidden information	TORVALDS, LINUS
11069497	Not Issued	71	02/28/2005	Method for translating instructions in a speculative microprocessor	TORVALDS, LINUS
11077623	7331041	150	03/10/2005	METHOD OF OPTIMIZING COMPILER VS INTERPRETER MODE OPERATION IN A CODE TRANSLATOR BASED UPON AMOUNT OF TIME SPENT IN EACH MODE	TORVALDS, LINUS
11507779	7404181	150	08/21/2006	SWITCHING TO ORIGINAL CODE COMPARISON OF MODIFIABLE CODE FOR TRANSLATED CODE VALIDITY WHEN FREQUENCY OF DETECTING MEMORY OVERWRITES EXCEEDS THRESHOLD	TORVALDS, LINUS
11524044	Not Issued	41	09/19/2006	A METHOD AND SYSTEM FOR STORING AND RETRIEVING A TRANSLATION OF TARGET PROGRAM INSTRUCTION FROM A HOST PROCESSOR USING FAST LOOK-UP OF INDIRECT BRANCH DESTINATION IN A DYNAMIC TRANSLATION SYSTEM	TORVALDS, LINUS
12177836	Not Issued	19	07/22/2008	SWITCHING TO ORIGINAL CODE COMPARISON OF MODIFIABLE CODE FOR TRANSLATED CODE VALIDITY WHEN FREQUENCY OF DETECTING MEMORY OVERWRITES EXCEEDS THRESHOLD	TORVALDS, LINUS

Inventor Search Completed: No Records to Display.

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